

### **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Sectronic Version v18 Stylesheet Version v18.0

> Title of Invention

Store-to-Load Forwarding Buffer Using Indexed Lookup

Application Number:

10/615101

**Confirmation Number:** 

8643

First Named Applicant:

Michael Filippo

Attorney Docket Number: 550089400

Art Unit:

2121

Search string:

( 4521851 or 4594660 or 5404470 or 5465336 or 5467476 or 5651125 or 5710902 or 5717883 or 5748978 or 5799165 or 5835747 or 5884060 or 5884061 or 5923862 or 5996068 or 6016540 or 5276828 or 5440752 or 5487156 or 5490259 or 5557763 or 5625835 or 5652859 or 5742791 or 5761712 or 5768555 or 5802588 or 5832297 or 5887152 or 6122727 or 5619662 or 5781752 or 6212622 or 6212623 or 5850533 or 6266744 or 6393536 or 6108770 or 6502185 or 6542984

or 6651161 ).pn.

#### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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œ	2	4594660	1986-06-10	Guenthner et al.			
K	3	5404470	1995-04-04	Miyake			
X	4	5465336	1995-11-07	lmai et al			
W.	5	5467476	1995-11-14	Kahle et al			
K	6	5651125	1997-07-22	Witt et al	1		
R	7	5710902	1998-01-20	Sheaffer et al			
æ	8	5717883	1998-02-10	Sager			
P	9	5748978	1998-05-05	Narayan et al			
肥	10	5799165	1998-08-25	Favor et al			
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区	14	5923862	1999-07-13	Nguyen et al	
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W	27	5802588	1998-09-01	Ramagopal et al	
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P.	29	5887152	1999-03-23	Tran	
R	30	6122727	2000-09-19	Witt	
W.	31	5619662	1997-04-08	Steely Jr et al	
K	32	5781752	1998-07-14	Moshovos et al	
	33	6212622	2001-04-03	Witt	
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D0	35	5850533	1998-12-15	Panwar et al	
	36	6266744	2001-07-24	Hughes et al	
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TP.	38	6108770	2000-08-22	Chrysos et al	
R	39	6502185	2002-12-31	Keller et al	
K	40	6542984	2003-04-01	Keller et al	
JC.	41	6651161	2003-11-18	Keller et al	

# Remarks

Note: Remarks are not for responding to an office action.

Other art will be submitted on a Form PTO 1449.

Signature	
Examiner Name	Date

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### **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

TRANSPECTRONIC Version v18 Stylesheet Version v18.0

> Title of Invention

Store-to-Load Forwarding Buffer Using Indexed Lookup

**Application Number:** Confirmation Number: 10/615101

8643

First Named Applicant:

Michael Filippo Attorney Docket Number: 550089400

Art Unit:

2121

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( 6622237 or 6622235 or 5555432 or 5655096

or 5844059 or 5941983 or 5987594 or 6308259 or 6108769 or 6065105 or 5978900 or 6076153 or 6321328 or 5778219 or 5794029 or 5915117

or 6336178 or 5751983 or 5781790 or 5751985 or 6098166 or 5724536 or 6070238 or

5802575 ).pn.

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#### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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OC.	<b>2</b> ·	6622235	2003-09-16	Keller et al			
X.	3	5555432	1996-09-10	Hinton et al			
X.	4	5655096	1997-08-05	Branigin			
Øe.	5	5844059	1999-03-16	Favor et al			
PC	6	5941983	1999-08-24	Gupta et al			
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W.	8	6308259	2001-10-23	Witt			
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D.	10	6065105	2000-05-16	Zaldi et al	}		
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灰	16	5915117	1999-06-22	Ross et al	
	17	6336178	2002-01-01	Favor	
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W	20	5751985	1998-05-12	Shen et al	
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	23	6070238	2000-05-30	Feiste et al	
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# Signature

Examiner Name	Date
billian	21-DEC2095

ATTY. DKT. NO. 5500-89400 SERIAL NO. 10/615,101 Form PTO-1449 (modified) List of Patents and Publication **GROUP: 2121** SAPPLICANT: Filippo, et al. For Applicant's Information Disclosure Statement FILING DATE: July 8, 2003 (Use several sheets if necessary) U.S. PATENT DOCUMENTS FILING DATE IF **CLASS** SUB DATE NAME DOCUMENT NUMBER REF. EXAM. APPROPRIATE **CLASS** DES. INITIALS FOREIGN PATENT DOCUMENTS TRANSLATION **CLASS** SUB COUNTRY DOCUMENT NUMBER DATE REF. EXAM. YES/NO **CLASS INITIALS** DES. WO 4/25/96 A1 96/12227 wo 3/2/00 00/11548 A2 EP 5/1/96 709 770 **A3** WO 7/31/97 A4 97/27538 GB **A5** 3/1/95 2 281 422 OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Moshovos, et al., "Dynamic Speculation and Synchronization of Data Dependences," ACM, Published June 2, **A6** 1997, XP-000738156, pp. 181-193. Chrysos, et al., "Memory Dependence Prediction Using Store Sets," IEEE, Published June 27, 1998, XP-**A7** 000849912, pp. 142-153. Leibholz, et al., "The Alpha 21264: A 500 MHz Out-of-Order Execution Microprocessor," IEEE, published A8 February 23, 1997, XP-000751757, pp. 28-36. Popescu, et al., "The Metaflow Architecture," IEEE Micro, June 1991, pp. 10-13 and 63-73. A9 Gwennap, "Digital 21264 Sets New Standard," Microprocessor Forum, Vol. 10, NO. 14, Microdesign A10 Resources, October 28, 1996, pp. 103-108. Mike Johnson, "Superscalar Microprocessor Design," Advanced Micro Devices, Prentice Hall 1991, pp. 130-A11 132, 147-163. Moshovos, et al., "Streamlining Inter-operation Memory Communication via Data Dependence Prediction," A12 IEEE Proceedings of Micro-30, December 1997, 11 pages. Moshovos, et al., "Speculative Memory Cloaking and Bypassing," October 1999, 15 pages. A13

**EXAMINER:** 

A14

DATE CONSIDERED:

21-DEC-2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Gopal, et al., "Speculative Versioning Cache," February 1998, 11 pages.



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Electronic Version v18
Stylesheet Version v18.0

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8643

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Michael Filippo

Attorney Docket Number: 5500-89400

EE00 00400

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
C	1	6694424	2004-02-17	Keller et al	.:		
R	2	6266744	2001-07-24	Hughes et al			
	3	6393536	2002-05-21	Hughes et al		•	
X X X	4	6108770	2000-08-22	Chrysos et al			
OC.	5	6502185	2002-12-31	Keller et al	}		
K	6	6542984	2003-04-01	Keller et al			
X	7	5850533	1998-12-15	Panwar			
	8	6651161	2003-11-18	Keller et al			

# Signature

Examiner Name	Date
Mala	21-DEC-2005